



# CSP MOSFET 数据手册

## NP12045ECA

**CSP 12V Typ 4.5mΩ Dual N-Channel MOSFET**

**Rev. 1.1 Version**

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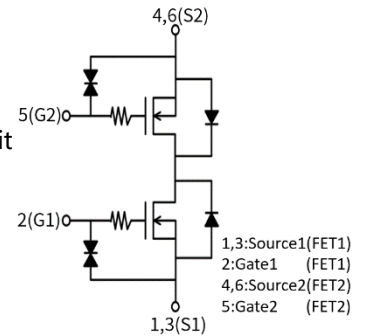
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## CSP 12V Typ 4.5mΩ Dual N-Channel NP12045ECA

### 产品特性 Features

- |                       |  |
|-----------------------|--|
| ◆ 2.5V 驱动电压           | 2.5 V Drive                                      |
| ◆ 共漏极结构, 设计方便         | Common-Drain configuration for design simplicity |
| ◆ 低的导通电阻              | Low source-source ON resistance                  |
| ◆ 2kV HBM 栅极静电保护      | 2kV HBM ESD Diode-Protected Gate                 |
| ◆ 无铅引脚, 无卤化符合 RoHS 要求 | Pb-Free, Halogen Free and RoHS compliance        |



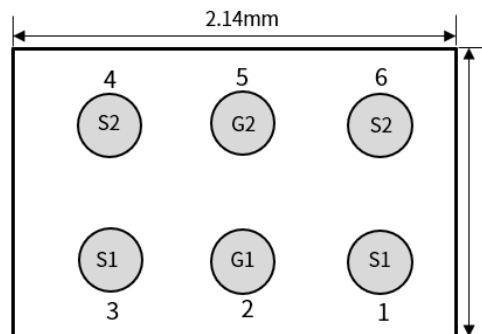
原理图

Schematic diagram

### 应用领域 Applications

- |              |  |
|--------------|--|
| ◆ 单节锂电池充放电开关 | 1-Cell Lithium-ion battery charging and discharging switch |
| ◆ 锂电池保护开关    | Battery protection switch                                  |

### 关键参数与封装信息 Key Performance and Package Parameters



1,3:Source1(FET1)  
 2:Gate1 (FET1)  
 4,6:Source2(FET2)  
 5:Gate2 (FET2)  
 Package Type: CSP  
 Size:2.14 (mm)×1.40(mm)×0.30(mm)

产品型号 Part No.	封装 Package	源极到源极电压 V <sub>SS</sub>	源极电流最大值 I <sub>S</sub> Max	导通电阻 R <sub>SS(on)</sub>	印记 Markin
NP12045ECA	CSP	12V	9A	4.5mΩ @ 4.5 V	4A
				5.4mΩ @ 3.8 V	
				6.1mΩ @ 3.1 V	
				7.2mΩ @ 2.5 V	

Item	Marking	Tape&Reel	Lable
File Name	BPMC-02	BPTC-07A	BPLC-01

## 最大额定值 Maximum Ratings<sup>①</sup>

参数 Parameter	符号 Symbol	数值 Value	单位 Unit
源极-源极击穿电压 Source-Source Breakdown Voltage	$BV_{SSS}$	12	V
栅极-源极电压 Gate to Source Voltage	$V_{GS}$	$\pm 8$	V
直流源极电流 Source Current (DC)	$I_S$	9.0	A
脉冲源电极电流 Source Current (Pulse) $PW \leq 10\mu s, Duty\ Cycle \leq 1\%$	$I_{Sp}$	90	A
耗散功率 Power Dissipation	$P_D$	0.5	W
结温 Operating Junction Temperature	$T_J$	-55...+150	$^{\circ}C$
存储温度 Storage Temperature	$T_{Stg}$	-55...+150	$^{\circ}C$

① 超过最大额定值表中列出的使用条件可能会损坏芯片。如果超过这些限制值中的任何一个，则芯片的功能可能无法保证，芯片可能发生损坏并影响可靠性。

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

## 热阻特性 Thermal Resistance<sup>②</sup>

参数 Parameter	符号 Symbol	数值 (最大) Max. Value	单位 Unit
结到环境热阻 Thermal Resistance Junction to Ambient	$R_{\theta JA}$	250	$^{\circ}C/W$

② 芯片表面贴于 70mmX70mm 环氧树脂 PCB 板。

Surface mounted on Mounted on 70mmx70mm FR4 board

## 电气特性 Electrical Characteristics ( $T_J = 25^\circ\text{C}$ unless otherwise noted)

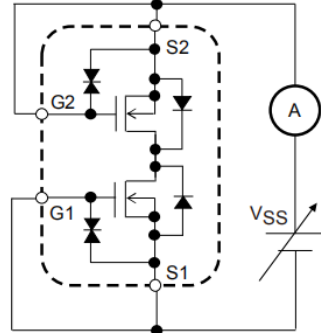
项目③ Parameter	符号 Symbol	测试条件 Conditions	数值 Value			单位 Unit
			Min.	Typ.	Max.	
源极-源极击穿电压 Source to Source Breakdown Voltage	$V_{(BR)SSS}$	$I_S = 1\text{ mA}$ , $V_{GS} = 0\text{ V}$ Test Circuit 1	12	-	-	V
零栅压下源极漏电流 Zero Gate Voltage Source Current	$I_{SSS}$	$V_{SS} = 10\text{ V}$ , $V_{GS} = 0\text{ V}$ Test Circuit 1	-	-	1.0	$\mu\text{A}$
栅极-源极漏电流 Gate to Source Leakage Current	$I_{GSS1}$	$V_{GS} = \pm 5\text{ V}$ , $V_{SS} = 0\text{ V}$ Test Circuit 2	-	-	$\pm 1.5$	$\mu\text{A}$
栅极-源极漏电流 Gate to Source Leakage Current	$I_{GSS2}$	$V_{GS} = \pm 8\text{ V}$ , $V_{SS} = 0\text{ V}$ Test Circuit 2	-	-	$\pm 10$	$\mu\text{A}$
阈值电压 Gate Threshold Voltage	$V_{GS(th)}$	$V_{SS} = 6\text{ V}$ , $I_S = 1\text{ mA}$ Test Circuit 3	0.4	-	1.0	V
源极到源极直流导通电阻 Static Source to Source On-State Resistance	$R_{SS(on)}$	$I_S = 5\text{ A}$ , $V_{GS} = 4.5\text{ V}$ Test Circuit 4	1.6	4.5	5.9	$\text{m}\Omega$
		$I_S = 5\text{ A}$ , $V_{GS} = 3.8\text{ V}$ Test Circuit 4	1.7	5.4	7.5	$\text{m}\Omega$
		$I_S = 5\text{ A}$ , $V_{GS} = 3.1\text{ V}$ Test Circuit 4	1.8	6.1	8.0	$\text{m}\Omega$
		$I_S = 5\text{ A}$ , $V_{GS} = 2.5\text{ V}$ Test Circuit 4	2	7.2	10.0	$\text{m}\Omega$
源极到源极正向压降 Forward Source to Source Voltage	$V_{F(S-S)}$	$I_S = 1\text{ A}$ , $V_{GS} = 0$ Test Circuit 7	0.4	0.62	1.0	V

③ 产品的参数性能按照上述表格参数所描述，除非有额外的说明。如果产品工作在其他不同的条件下，产品的电学性能可能并不遵守上述参数。

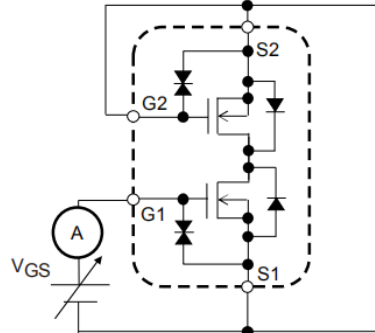
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

## 测试电路范例 Test Circuits Example

Test Circuit 1  
V<sub>SS</sub> / I<sub>SSS</sub>

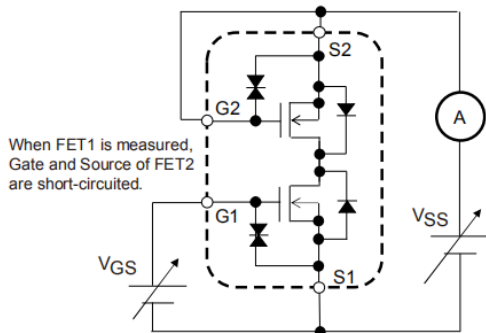


Test Circuit 2  
I<sub>GSS</sub>



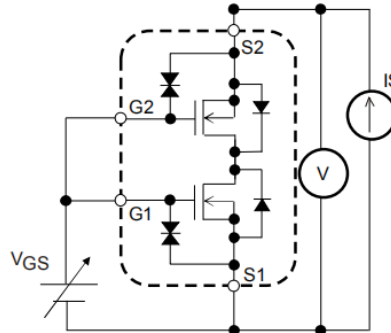
When FET1 is measured, Gate and Source of FET2 are short-circuited.

Test Circuit 3  
V<sub>GS(th)</sub>

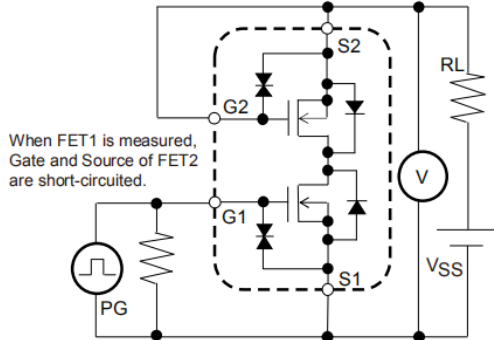


When FET1 is measured, Gate and Source of FET2 are short-circuited.

Test Circuit 4  
R<sub>SS(on)</sub>

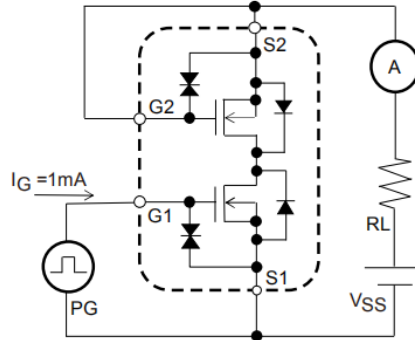


Test Circuit 5  
t<sub>d(on)</sub>, t<sub>r</sub>, t<sub>d(off)</sub>, t<sub>f</sub>



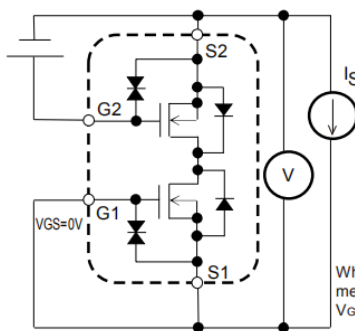
When FET1 is measured, Gate and Source of FET2 are short-circuited.

Test Circuit 6  
Q<sub>g</sub>



When FET1 is measured, Gate and Source of FET2 are short-circuited.

Test Circuit 7  
V<sub>F(S-S)</sub>



When FET1 is measured, +4.5V is added to V<sub>GS</sub> of FET2.

## 特征电学和温度曲线 Typical Electrical and Thermal Characteristics

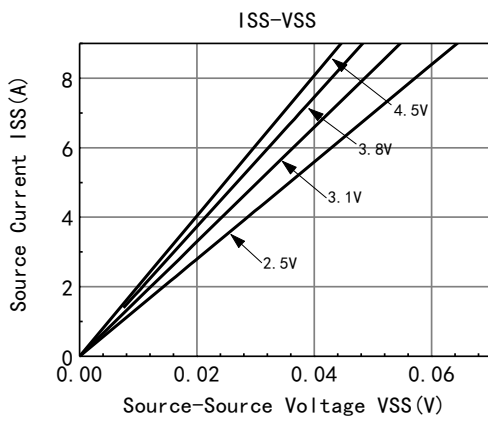


Figure 1: On-Region Characteristics

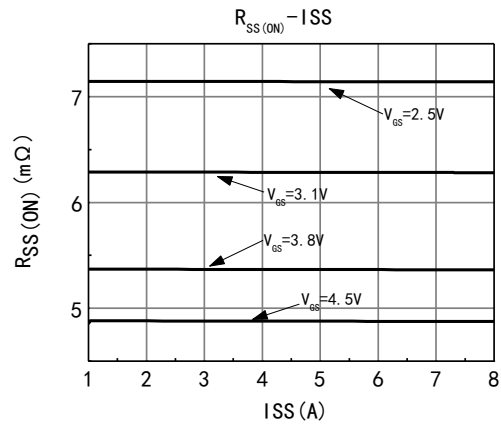


Figure 2: On-Resistance vs. Source Current and Gate Voltage

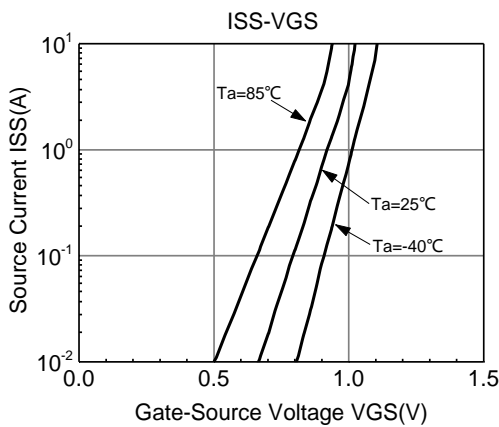


Figure 3: Typical Transfer Characteristics

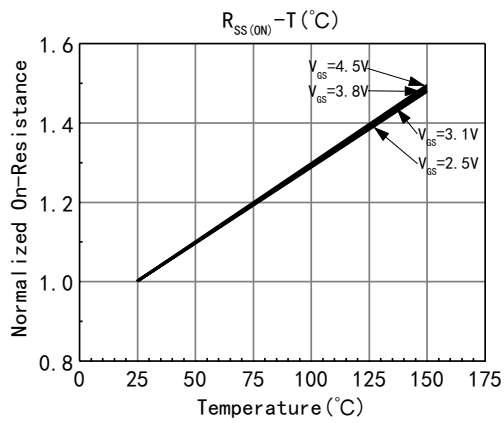


Figure 4: On-Resistance vs. Junction Temperature

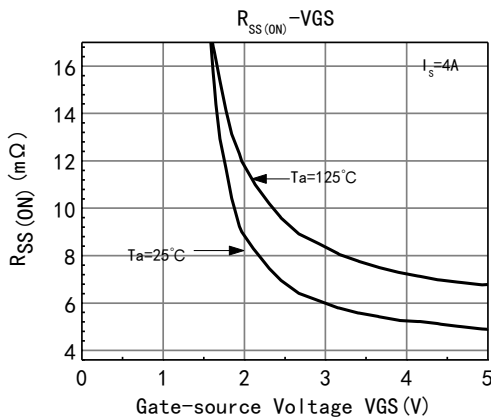


Figure 5: On-Resistance vs. Gate-Source Voltage

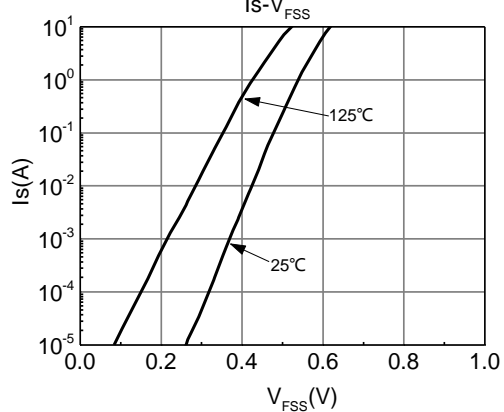


Figure 6: Forward Source to Source Characteristics

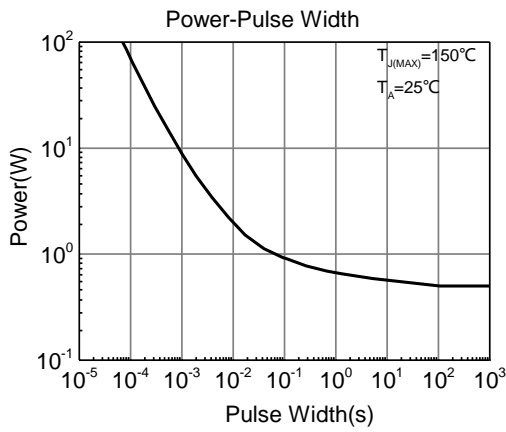


Figure 7: Single Pulse Power Rating Junction to Ambient

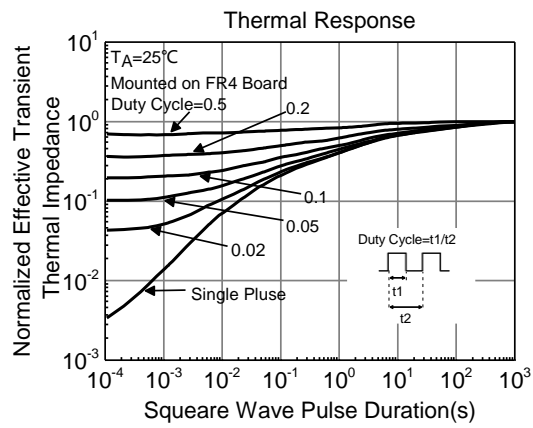


Figure 8: Normalized Maximum Transient Thermal Impedance

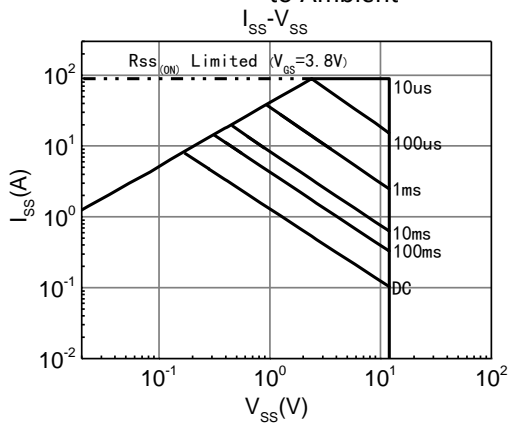
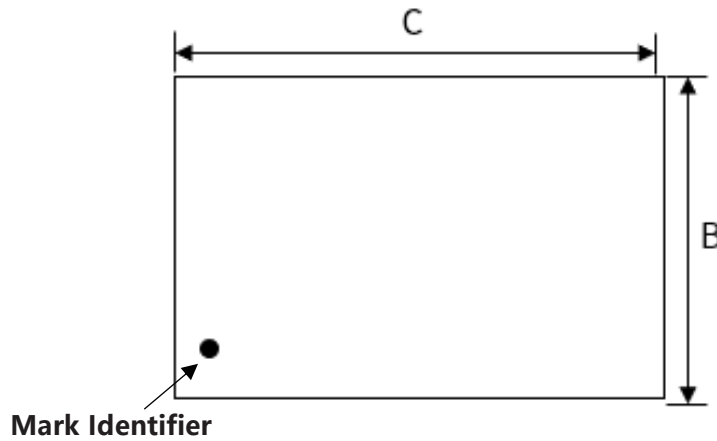


Figure 9: Safe Operating Area

## 封装尺寸 Package Dimensions:

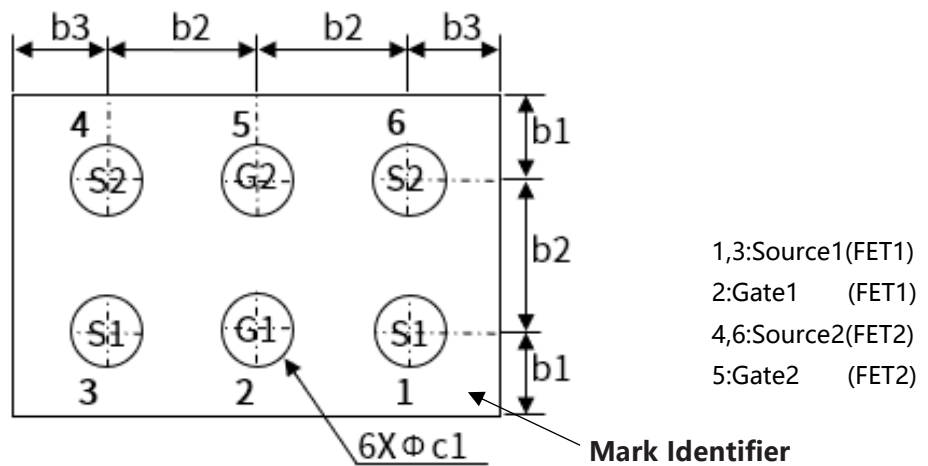
Top View



Side View



Bottom View



Symbol	Dimensions In Millimeters			Dimensions In Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	0.27	0.30	0.33	0.0106	0.0118	0.0130
B	1.35	1.40	1.45	0.0531	0.0551	0.0571
C	2.08	2.14	2.20	0.0819	0.0843	0.0866
b1	0.335	0.375	0.415	0.0132	0.0148	0.0163
b2	0.61	0.65	0.69	0.0240	0.0256	0.0272
b3	0.38	0.42	0.46	0.0150	0.0165	0.0181
c1	0.27	0.30	0.33	0.0106	0.0118	0.0130



## 历史版本 Version history

版本号	时间	修改内容
V1.0	2022 年 06 月	初始版本
V1.1	2023 年 11 月	1.更新 Ron 典型值 2.Pin1 点改成 Mark Identifier